

PULSEWIDTH CONTROL LOOP DEVICE WITH COMPLEMENTARY SIGNALS

Abstract

The present invention provides one pulsewidth control loop (PWCL) device with complementary signals. The PWCL device includes one control stage circuit, one buffer chain, one complementary circuit, two charge pumps, and one comparator. The control stage circuit is used to receive a clock signal and the control signal of the comparator, and output a signal to the buffer chain. The buffer chain is used to receive the output signal from the control stage circuit and output a signal to the complementary circuit. The complementary circuit is used to receive the output signal from the buffer chain and output two complementary signals. Each of the two charge pumps is used to receive one of the output signals from the complementary circuit and output a signal to be one of the inputs of the comparator. The comparator is used to receive the output signals from each of the two charge pumps. Then, the comparator outputs a signal and feedbacks to be one of the input signals of the control stage circuit. The control loop device utilizes the complementary signals to adjust the pulsewidth and increase the stability of the control loop device.